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ANNUAL REPORT

TO THE

OFFICE OF NAVAL RESEARCH CODE 414 800 N. QUINCY ST. ARLINGTON, VA 22217

ON

APPLICATIONS OF JOSEPHSON
JUNCTION SQUIDS AND ARRAYS
CONTRACT NO. NOO014-81-C-0615
SEPTEMBER 1981 - SEPTEMBER 1982

PREPARED BY

A.H. SILVER
ADVANCED PRODUCTS LABORATORY
APPLIED TECHNOLOGY DIVISION
TRW



NOVEMBER 1982

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Defense Sector

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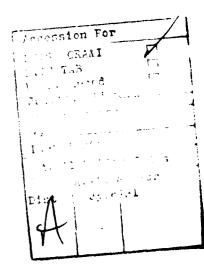
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experimental design, and fabricat	tion of the devic	e including tests of the
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## 1. INTRODUCTION

This is the Annual Progress Report for Contract No. N00014-81-C-0615, "Application of Josephson Junction SQUIDs and Arrays." covering the period from 1 September 1981 through 30 September 1982. The objectives of this program are to define and demonstrate applications of SQUIDs and SQUID arrays via analysis and experiment. The goals of this contract were to investigate and demonstrate the properties of a SQUID voltage-controlled-oscillator (VCO). These goals were met by 1) analysis and simulation of a voltage-clamped resistive SQUID and a voltage-clamped dc SQUID, 2) design, fabrication and measurement of appropriate microwave matching transformer, and, 3) design, fabrication, and measurement of a SQUID VCO. We report on progress in these three areas.

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### 2. WORK PLAN

This is a new contract for a projected multi-year investigation of Superconducting QUantum Interference Device (SQUID) arrays and their application to microwave and gigabit technology. The focus of this contract is the investigation and demonstration of a SQUID voltage-controlled oscillator (VCO). The array is then projected to increase the available power and operating impedance. The approach utilizes monolithic superconducting integrated circuits which were designed, fabricated, and tested at TRW.

Three tasks were undertaken in the first year:

- Task 1. Analysis of dc SQUID Arrays
- Task 2. Design of dc SQUID Generator
- Task 3. SQUID VCO Fabrication and Measurement.

A number of the steps are very similar to those required for a contract with the Naval Research Laboratory, "Demonstration of a SQUID Parametric Amplifier." Such steps were carried out jointly and are reported to both agencies. TRW is concurrently conducting Independent Research and Development on Advanced Josephson Devices. This project is principally concerned with development of a Josephson integrated circuit fabrication capability. Some of the activities of that program which impact this contract are discussed here.

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### 3. PROGRESS

### 3.1 Transformer Design

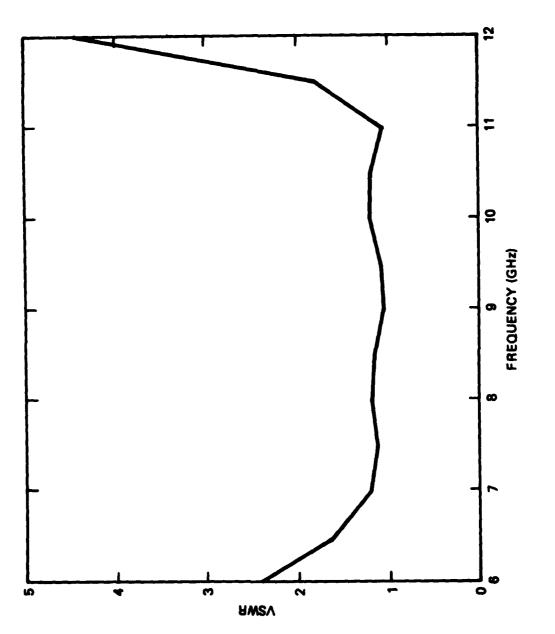
Since the SQUID VCO is necessarily a very low impedance device, a suitable transformer is an essential ingredient for evaluating the basic VCO, particularly with respect to available power. In order to carry out this transformation in both impedance and dimension in a reproducible and predictable manner, the transformer is integrated with the active circuitry.

The basic architecture of the transformer was presented in the contract proposal. We have refined the design for the TRW fabrication process and reconsidered the problems of launching from  $50\Omega$  coaxial cable into  $50\Omega$  coplanar line, dimensional step from 1 mm coplanar line at  $50\Omega$ , and construction of capacitors in the lumped element impedance transformer. The electrical design was optimized numerically using COMPACT with a computed VSWR < 1.25 over the 8-12 GHz band (Figure 1). Figure 2 shows the equivalent electrical circuit and the geometrical layout.

A test of the effect of changing dimensions in  $50\Omega$  coplanar line was performed by fabricating three  $50\Omega$  transmission lines: a continuous coplanar line at mm dimensions, a sharply stepped coplanar line, and a short, tapered transition as shown in Figure 3. The measured performance showed no significant differences between the three lines. Therefore, the input LC circuit in the transformer intended to tune out the inductance of the step in the coplanar line was deleted.

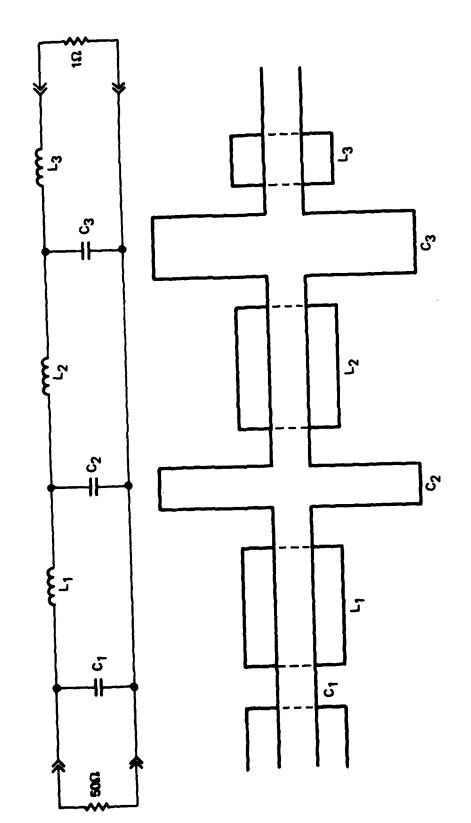
The transformer was designed to be fabricated on a 2 inch silicon wafer, 0.015 in. thick, with a minimum line width of  $50\mu m$ . An OSM microwave launcher drives the  $50\Omega$  coplanar line which has a center conductor width of  $50\mu m$ , ground plance spacing of  $30\mu m$ , and an effective dielectric

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FIGURE 1. Computed voltage standing wave radio (VSWR) as a function of frequency for transformer circuit shown in Figure 3.



circuit parameters are  $C_1$  = 0.6 pF,  $L_1$  = 442 pH,  $C_2$  = 1.6 pF, Equivalent circuit (top) and physical layout (not to scale) of X-band transformer designed and simulated. The lumped  $L_2 = 108 \text{ pH}$ ,  $C_3 = 8.6 \text{ pF}$ ,  $L_3 = 22 \text{ pH}$ . Figure 2.

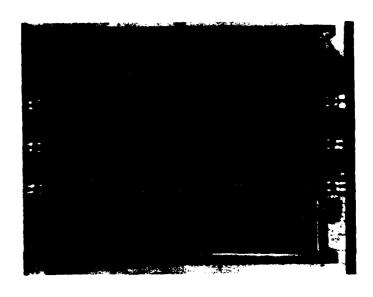


Figure 3. Photograph of the  $50\Omega$  coplanar lines used to test dimensional changes.

constant of 6.4. The reduction in  $\varepsilon$  and its dependence on linewidth results from the finite thickness of the Si substrate. The  $1\Omega$  microstrip in the transformer is  $50\mu m$  wide and separated from the Nb ground plane by 50nm Nb $_20_5$  and 200nm SiO. It has an effective  $\varepsilon$ =13.

The transformer design incorporates short (<  $\lambda/4$ ) sections of coplanar and microstrip lines which act as lumped inductors and capacitors, respectively. The inductive lines are  $50\Omega$  coplanar lines terminated by  $1\Omega$  microstrip, with L=Z<sub>o</sub> $\ell/V$ , where Z<sub>o</sub> is the line impedance,  $\ell$  the line length, and v the propagation velocity. Capacitors are  $1\Omega$  and  $0.5\Omega$  microstrip either terminated by  $50\Omega$  coplanar or used as open parallel stubs. The capacitance of short, open lines is given by  $C=\ell/Z_ov$ .

Photolithographic patterns were defined as 4-level masks and produced at the TRW Microelectronics Center for the combined dimensional and impedance transformer. For the dimensions and tolerances required, masks were fabricated directly at the reticle level in an Electromask pattern generator. Figure 4 shows the computer-generated composite of three 1 cm x 2 cm chips which were fabricated on 2" silicon wafers, .015" thick. Each chip has 2 coplanar inputs with tapered lines. On one chip the reduced width coplanar line couples directly through; on the second chip, the transformers from each end are connected for in-out transmission measurements. The third chip has each coplanar line and transformer terminated with a matched resistive load. Figure 5 is an expanded plot of the transformer and terminating resistor on the third chip.

The transformers were fabricated in a 1 cm x 2 cm format on 2" diameter silicon wafers with an insulating  $\mathrm{SiO}_2$  surface. Three test chips are produced on each wafer. Following fabrication, the wafers were cleaved with a modified Tempress scriber and the chips mounted in a holder suitable for connection to an OSM coaxial bulkhead launcher. Measurements are made at T=4K with an HP-8410 Network Analyzer. The data are in the form of the scattering matrix coefficients  $\mathrm{S}_{11}$  and  $\mathrm{S}_{12}$ . Only the magnitude of S is reported since the phase is difficult to calibrate with long coaxial lines into the helium dewar.

FIGURE 5. Printout of composite masks for the  $50\alpha$  to  $1\Omega$  transformer terminated in the  $1\Omega$  resistor at the center. TERMINATION PRESISTORS IN MICROSTRIP -

The test transformer chips are mounted with a coaxial connector at each end as shown in Figure 6. As anticipated, connecting the coplanar input to the bulkhead connector was a significant problem. An acceptable solution is to use in solder to connect gold ribbon from the connector centerpin and bulkhead to the coplanar lines. To facilitate soldering to the Nb film, a gold film is evaporated over the Nb. This is done before the Nb is patterned. Figure 7 shows the response of the simple tapered coplanar lines at 4 kelvin; Figure 8 the response of the double-ended transformer, in which the  $50\Omega$  coplanar line is transformed to  $1\Omega$  microstrip at the center of the chip, and then transformed back to  $50\Omega$  coplanar at the other end of the chip. We have not measured good S-parameter characteristics with the terminated transformers and we attribute this to a problem in making good contact with the resistors.

# 3.2 Analysis of Voltage-Clamped SQUIDs

We have investigated the voltage-clamped SQUID in two forms appropriate to the voltage-controlled oscillators, the resistive SQUID of Figure 9 and the dual resistive SQUID with common voltage-biasing resistor which we call the voltage clamped dc SQUID (Figure 10). To the best of our knowledge, neither of these devices incorporating the RSJ model with appropriate capacitance has been fully investigated and thus computer simulation was undertaken to predict the operating characteristics and parameterize the performance.

Analyses were performed with numerical simulations for preselected ranges of the device parameters. Computations of the transient response and subsequent Fourier spectra were performed on a Cyber 174/750 machine. The results are described in a paper which will be given at the 1982 Applied Superconductivity Conference and submitted for publication in the Conference Proceedings to appear in the IEEE Transactions on Magnetics in 1983. The manuscript, "SQUID Voltage-Controlled-Oscillator," is attached as a part of this report. (Appendix A).

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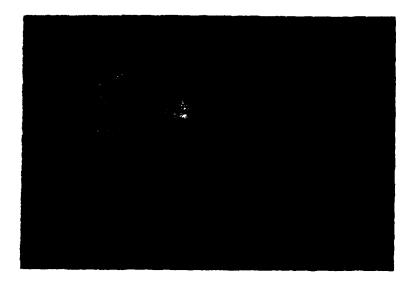
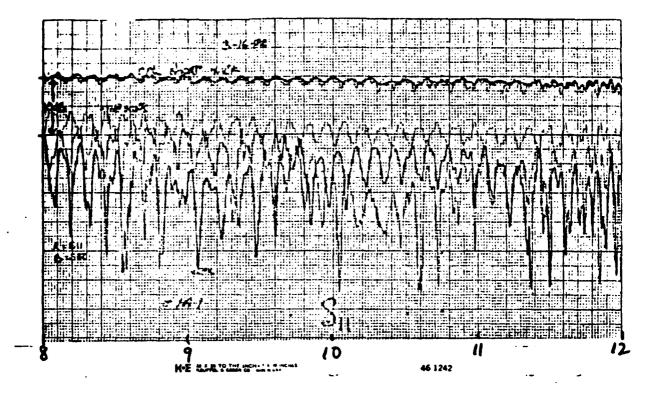


FIGURE 6. Photograph of transformer test chip mounted in a brass holder and connected to two OSM bulkhead connectors. The tapered coplanar lines at each end are visible in the photograph. The grey rectangle in the center of the chip is anodized Nb covered with SiO. The transformers are located in the same rectangle but are not clearly visible in the photograph.



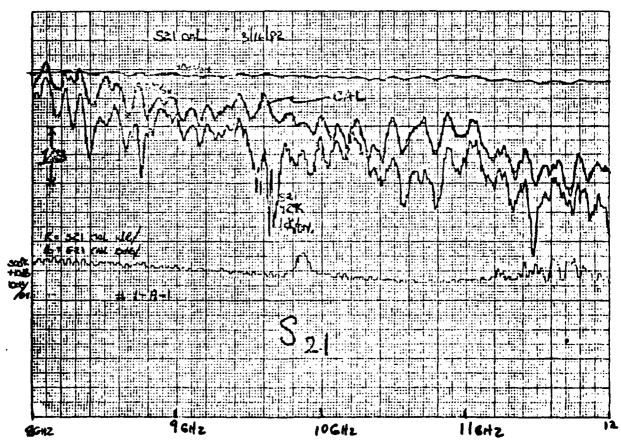
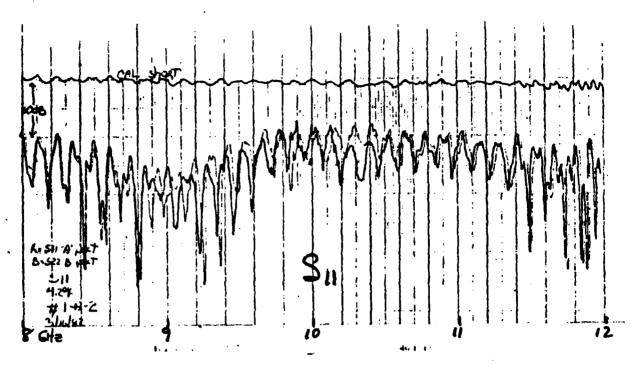


Figure 7. Response of 50n tapered coaxial lines on silicon substrate measured at 4K. The upper graph shows  $S_{11}$  (and  $S_{22}$ ) at 4K; the faint curve is  $S_{11}$  at 300K. The lower graph is  $S_{21}$  at 4K.



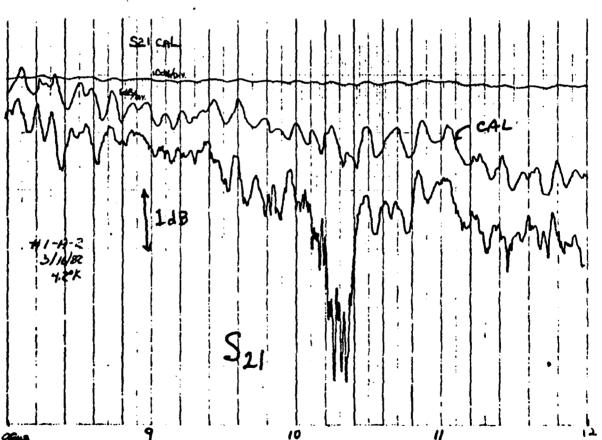


Figure 8. Response of the double-ended  $50\Omega$  to  $1\Omega$  transformers measured at 4K. The upper graph is  $S_{11}$  (and  $S_{22}$ ), the lower is  $S_{21}$ .

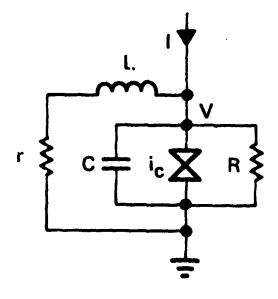


Figure 9. Resistive SQUID VCO. L is the SQUID inductance and r the small voltage-biasing resistor. R is the load resistance which sets the damping.

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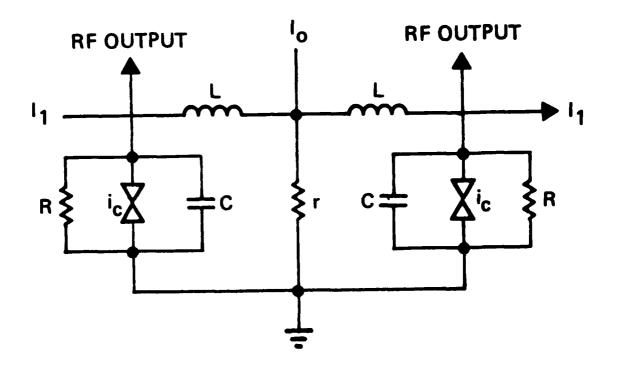


Figure 10. Voltage-clamped dc SQUID. The voltage-clamping resistor is r, and the load resistance is 2R.

### 3.3 SQUID VCO

### 3.3.1 Design

The VCO design placed the resistive SQUID at the low impedance end of the  $50\Omega$  to  $1\Omega$  transformer discussed above. Connection to the transformer is with  $1\Omega$  superconducting microstripline. Two devices are placed on each 1 cm x 2 cm chip; four chips are designed on each wafer. The SQUID was designed to utilize the Nb/Nb $_2$ 0 $_5$ /PbBi junctions in use at TRW, with PbIn interconnecting lines. Figure II shows the composite artwork for the VCO wafer. The structures at the top and bottom of each chip are test junctions and resistors.

The impedance of the SQUID,  $\sqrt{L/C}$ , is set at 0.3 $\Omega$ , such that the loading of the  $1\Omega$  transformer will result in Q=3.3. Design values are chosen to maximize  $\phi_0^2/2L\approx 10^4kT$  and set  $\beta=1$ , the SQUID resonance frequency equal to 9GHz, and the Josephson plasma resonance frequency also equal to 9GHz. Thus L = 5pH

$$C = 65pF$$

$$i_{\alpha} = 67\mu A$$

$$i_c = 67\mu A$$

$$j_o = 9A/cm^2$$

and the junction area is  $50\mu m \times 14.9\mu m = 745\mu m^2$ . The biasing resistance r is independently set with a design value near  $10^{-3}\Omega$ . This design assumes that the  $l\Omega$  load dominates over the internal quasiparticle resistance.

Because of the nature of the response for Q>2, we varied the design for each of the four chips on the wafer as follows:

VCO #	L(pH)	C(pF)	Q	i <sub>c</sub> (μΑ)	R <sub>d</sub> (Ω)
1	5	65	3.3	67	
2	10	32.5	1.7	33.5	
3	5	65	1.7	67	1
4	2.5	130	1.7	134	0.25

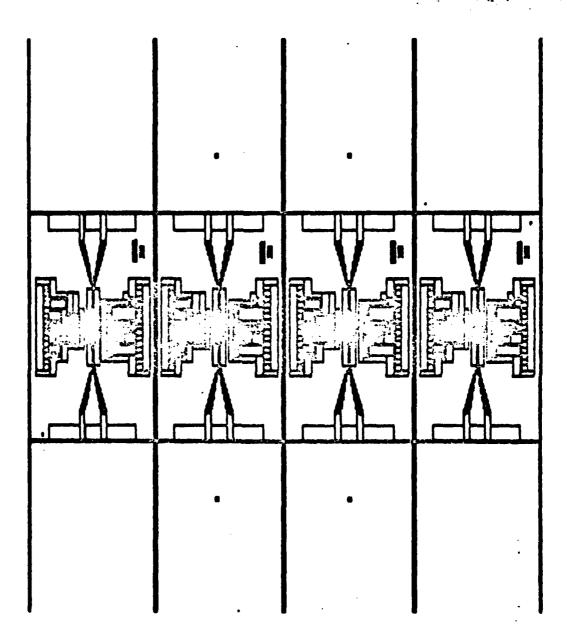


Figure 11a. Composite of seven masks designed for the four VCO circuits.

Test junctions and resistors are placed above and below each of the 2

VCO's per chip.

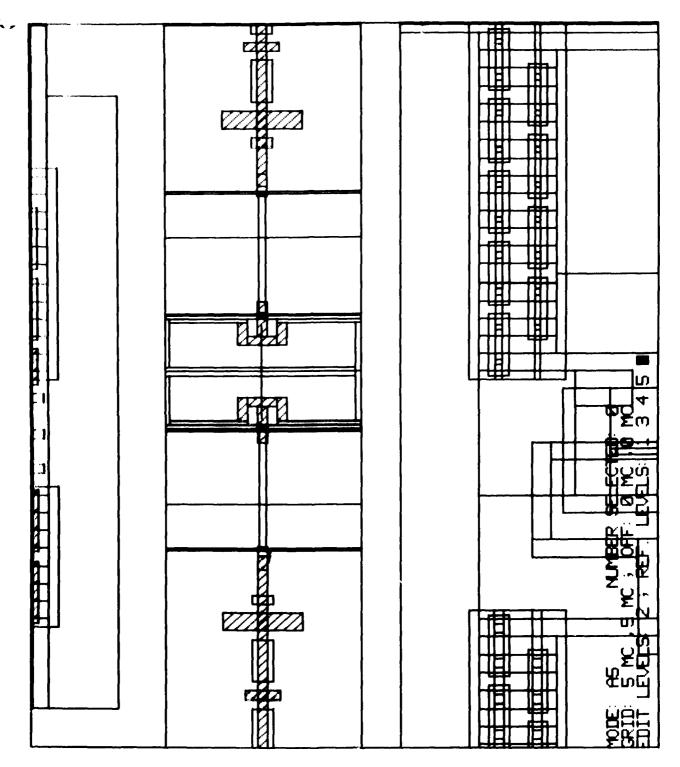


Figure llb. Composite of mask artwork for the central section of VCO #2.

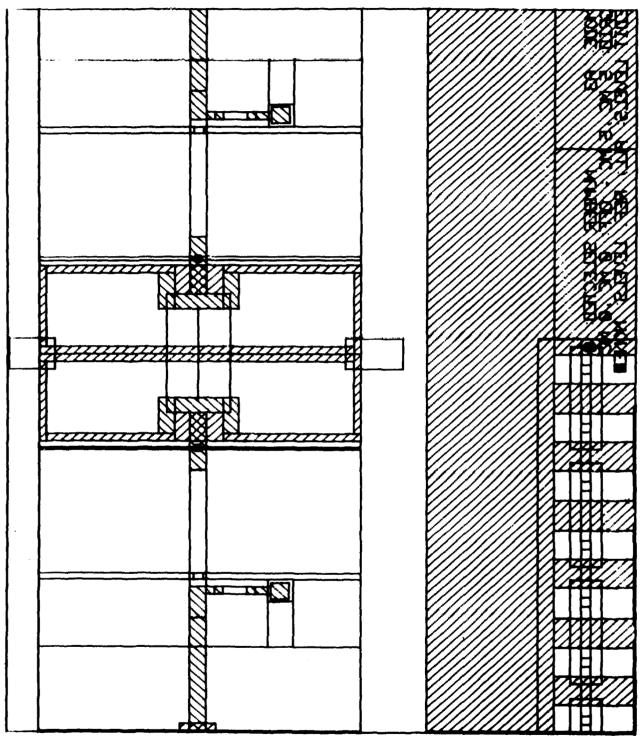


Figure 11c. Composite of mask artwork for

the central section of VCO #3.

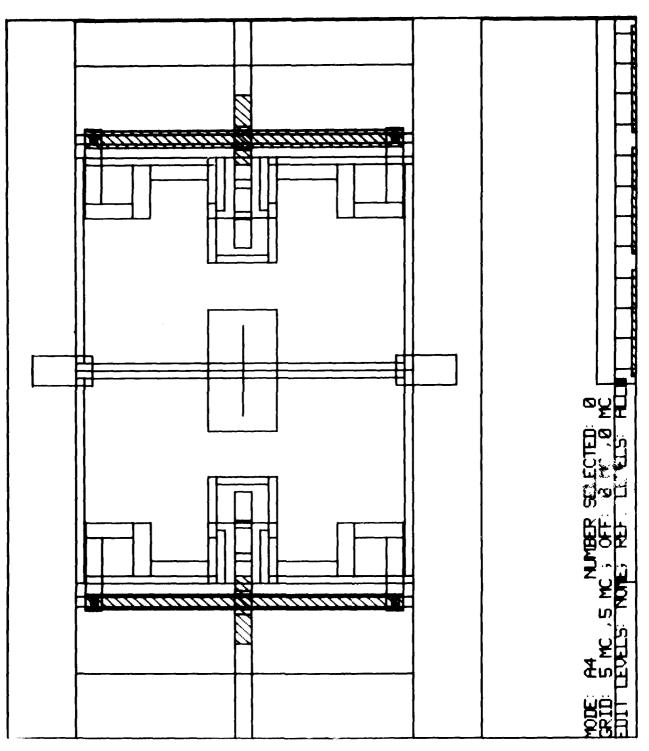


Figure 11d. Composite of the mask artwork for the central section of VCO #4.

R<sub>d</sub> is an additional damping resistance placed across the junction to lower the Q below 2. Although a substantial fraction of the microwave power is lost in the additional damping resistances in VCO 3, 4, they provide broad band damping compared to the transformed load of VCO 1, 2 which is well-defined only over the transformer bandwidth near 9GHz. In VCO 2 the inductance was doubled, halving the flux quantum energy, consequently reducing the available output power and the loaded Q.

### 3.3.2 Fabrication

The composite VCOs, which include the voltage-clamped resistive SQUIDs and matching transformer, were fabricated on 2" silicon wafers using Nb/Nb<sub>2</sub>O<sub>3</sub>/PbBi junctions, Au resistors, and PbIn interconnection lines. This represents a change from the sapphire substrates originally planned, but permits multiple chip fabrication and easy dicing of the wafer. Also, this method eliminates photoresist problems which occur at the edge of the wafer.

The change from sapphire to silicon substrates produced a problem holding substrates in the vacuum system. Since silicon is more fragile than sapphire, one cannot rigidly clamp the substrate for efficient heat sinking. Vacuum grease is effective at providing efficient heat transfer and mechanical support even for substrates mounted upside down. However, the grease is a potential contaminant for the process which occurs after the vacuum deposition. We have adopted a practice of scraping the grease and cleaning with hot xylene before doing lift-off in acetone. This technique appears manageable although clearly not optimum.

A more common problem was one of multi-layer deposition and processing where good electrical contact is required between layers. Three different vacuum systems were used for this project: an S-Gun sputtering system for Nb deposition, an ion gun system for junction formation, and an e-beam evaporator for other depositions. Only the ion gun system permitted in vacuo cleaning before deposition. The PbBi junction counter-electrode is thermally evaporated in the ion gun system immediately after reactive-ion-oxidation. However, we found significant problems with contacts

when films were deposited in e-beam system. On a temporary basis, we were thermally depositing other metals in the ion gun system, thereby permitting ion cleaning of the surface immediately prior to evaporation. This showed some improvement but was not completely successful, particularly for the alloy films. Also, this multiple use of the ion gun system led to contamination of the system with other materials prior to junction formation.

This problem was addressed more extensively under our IR&D program and we believe it has been resolved successfully. The e-beam system was modified mechanically and electrically to include an rf sputtering stage on which the substrates are mounted. RF sputter cleaning is then performed immediately prior to each new evaporation. Results with this new system are very encouraging. We have fabricated VCO circuits which show satisfactory junctions and connections. Voltage-biasing resistors in the  $50\text{--}100\text{m}\Omega$  range have been fabricated, one or two orders of magnitude larger than desirable. Figure 12 shows photographs of the SQUID section of the four different VCO chips after fabrication.

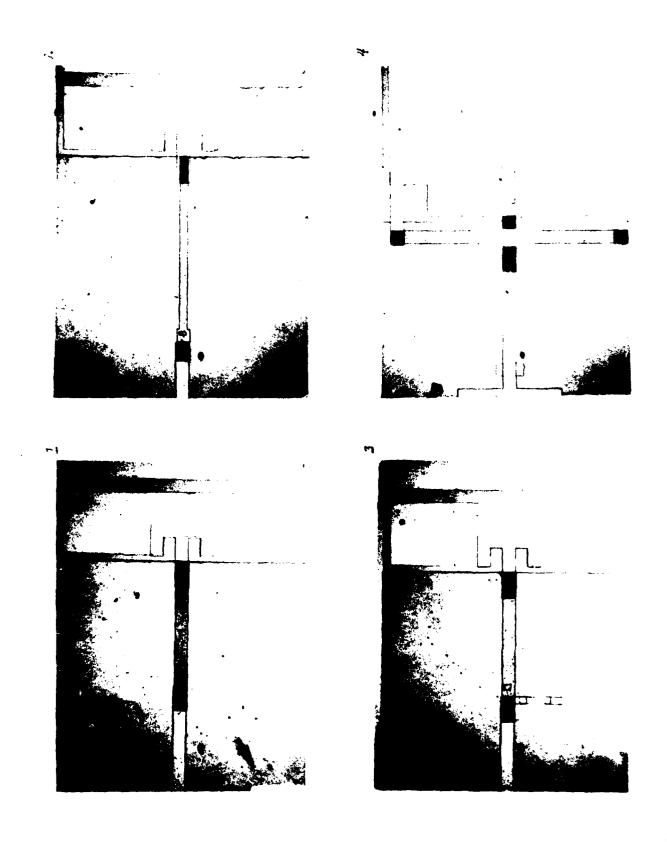


Figure 12. Photographs of the central sections of the four different VCO chips after fabrication.

## 4. CONCLUSIONS

Measurements show that the SQUID does generate microwave power, but the linewidth is much broader than expected. We believe this is a result of both external noise and internal instabilities of the type identified in 3.2. We are continuing to refine the measurements and SQUID parameters to demonstrate a well-behaved VCO.

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### SQUID VOLTAGE-CONTROLLED-OSCILLATOR\* A.H. Silver, R.D. Sandell, and J.Z. Wilcox TRW Space and Technology Group One Space Park, Redondo Beach, CA 90278

Abstract

We have investigated the SQUI voltage-controlled source of microwaves. the SQUID as a The low impedance "resistive" SQUID can be a relatively high power (wnW), tunable, and monochromatic source for both on-chip and off-chip applications. Studies of the time-dependent junction phase and the available power spectra as they vary with such device parameters as loaded Q and the SQUID-6=2\*Li\_/>
rules for a well-behaved oscillator. For a VCO fQ<2; for \$Q>2 degenerate parametric subharmonic oscillations and chaotic instabilities are observed. Power increase is suggested by the use of voltage-clamped dc SQUIDs and arrays.

Introduction

superconducting voltage-controlled-The oscillator (VCO) has potential applications as an integrated, on-chip source for such superconducting devices as analog-to-digital converters (sampling clock), rf SQUID magnetometers (excitation source), parametric amplifiers (pump source), quasiparticle mixers (local oscillator), and intracomputer communications. A source of sufficiently high power can be useful as an agile signal generator. This paper describes the SQUID VCO as the implementation of this source.

impetus for development microwave/millimeter wave signal generators in superconducting technology has its origin in the Josephson relations which predict a sinusoidal Josephson current under the application of a dc voltage which is linearly related to the Josephson frequency,  $\omega_{\rm j}=2\pi V/\phi$ . Unfortunately, problems of impedance and the dynamics of the junction phase have made it difficult to achieve the expected performance.

We argue here that the SQUID and SQUID arrays are the natural forms for a VCO. In order to achieve linear tuning, the source resistance must be small compared to the junction impedance. Such a small resistance connected directly across the junction would short out the Josephson oscillations, greatly reduce the signal voltage across the junction, and no power will be delivered to the load. However, in a resistive SQUID (Fig. 1) the voltage-biasing resistor is isolated from the junction by the SQUID inductance L. Thus, the dc voltage will be developed across the bias resistor, the ac voltage across the inductance, and the total voltage across the junction.

Tunnel junctions generally have lower internal conductance than microbridges, at least below the energy gap, and are desirable junctions to minimize

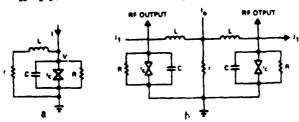


Figure 1. Equivalent circuit of SOUID VCO. (a) Resistive SQUID: (b) Voltage-clamped dc SQUID.

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internal losses. The shunt capacitance of the tunnel junction, which is frequently considered a serious shunting impedance, can be effectively parallel-tuned by the SQUID inductance. The load resistance, together with the internal junction resistance, will determine the loaded u of the VCO.

One expects the instantaneous signal bandwidth to be determined by the Johnson noise across the net (low frequency) resistance as a result of frequency modulation of the oscillator by the thermal voltage fluctuations. This has been predicted and verified for point contact devices as 6f=4nkTr/\$ 24.3x10 rT for a biasing resistance r at temperature T. This value is independent of frequency to the extent that the bias current is stable and noise-free.

The stable operating mode of a low  $\beta = 2\pi \text{Li}_{c} / \epsilon_{0}$ SQUID consists of regular flow of quantized flux of the internal circulating power in the SQUID is essentially that of changing quantized flux in the L at the oscillation frequency, inductance inductance L at the oscillation frequency,  $P_{\rm in} = 0 \, \omega_{\rm j}/4\pi L$ , while the power delivered to the load resistance is V/R where V=0  $\omega_{\rm j}/2\pi$ . Thus, the power in the load is  $P_{\rm R}=P_{\rm int}/\pi Q$  where Q=R/ $\omega$  L. Realistic values of L approximately 10 H project to ~10 W of power near 1032, with power increasing linearly with frequency at constant Q. This relation for Pp should be valid as Q decreases until the VCO is sufficiently loaded by R to reduce the ac voltage. Fundamental to achieving these power levels is small L and small Q. This requirement leads directly to load resistance values  $R=Q_{\omega}$  L of the order of 10  $^{\circ}\Omega$  for  $Q\sim 1$ , L $\sim 10^{-12} H_{\odot}$ and f.~10GHz. Proper impedance matching is clearly necessary for both on-chip and off-chip applications. Coherent arrays of SQUIDs which will increase the total impedance can increase the available power as well as simplify the impedance matching.

Resistive SQUID

We have investigated the voltage-clamped SQUID in two forms: the resistive SQUID (Fig. la) and the dual resistive SQUID with common voltage-biasing resistor which we call the voltage-clamped dc SQUID (Fig. 1b). The dynamical response of the SQUID shown in Fig. la was simulated numerically to predict the operating characteristics of the SQUID VCO. We envision R to be the load to which power is delivered. This circuit obeys the relation

$$\tilde{\theta} + (\eta_R + \eta_T)\tilde{\theta} + (1 + \eta_T \eta_R + \beta \cos \theta)\tilde{\theta} + \eta_T \beta \sin \theta = \tilde{\beta}_I + \eta_T \beta_I$$
 (1)

where  $\theta$  is the junction phase,  $\eta = \sqrt{L/C}/R = Q^{-1}$ ,  $\eta = r/\sqrt{L/C}$ ,  $\beta = 2\pi Li/\phi$ ,  $\beta_1 = 2\pi Li/\phi$ , and time is account in  $1/\omega = \sqrt{1C}$ . The voltage across the junction and load resistor R is 1-4 where and load resistor R is V=1 w 0/2 m. The last term on the right hand side of Eq. (1) is approximately the expected Josephson frequency  $\omega_1/\omega_1$  for I>i and is equivalent to a voltage source (rI) in series with r; the actual dc voltage across the junction is somewhat lower than rI and must be determined by either direct measurement or calculation of <0>.

The simulations derived  $\theta(t)$  and  $\theta(t)$  for The simulations derived  $\theta(t)$  and  $\theta(t)$  for selected values of  $\eta_{\rm c}$ ,  $\eta_{\rm R}$ ,  $\theta$ , and  $\theta_{\rm c}$  with  $\theta_{\rm c}=0$ . The results are essentially independent of  $\eta_{\rm c}$  for all reasonable values of interest and so  $\eta_{\rm c}=10^{-3}$  is used in all calculations reported here. This means that I=10<sup>3</sup>i and, hence,  $\theta_{\rm c}=10^{-3}$  at  $\omega_{\rm c}=\omega_{\rm c}$ . The transient response is dominated by  $\omega_{\rm c}$ , Q and  $\omega_{\rm c}$ . Figure 2 shows the time evolution of  $\theta$  and  $\theta$  for  $\theta=1$ , Q=1 and 5 for selected values of the voltage bias. For Q=1, the turn-on transient is short

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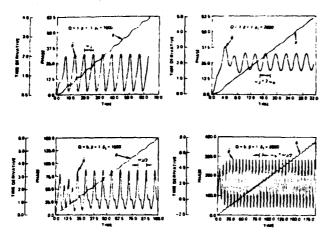


Figure 2. Selected transient response of  $\theta$  and  $\dot{\theta}$  for the resistive SQUID with  $\beta=1$  . Time is in units of  $\omega$ 

(approximately  $Q/\omega_0$ ) and associated with shock excitation of the resonant LC circuit, the fundamental frequency of the periodic response is  $\omega_J$ , and the periodicity in a smoothly stepped  $\theta$  is  $2\pi$ . If the Q is raised to 5, the transient replace is longer and the resonance frequency becomes further involved with the oscillation. At  $\omega_J/\omega_0^{-2}$ 1.5 ( $\beta_I=1500$ ), there is a strong subharmonic component evident in the time response. At  $\omega_J/\omega_0^{-2}$ 2 ( $\beta_I=2000$ ), the response appears to be totally at  $\omega_J=\omega_J/2$ , while for Q=1 there was no evident subharmonic response. Generally, for either large  $\beta$  or large Q, the response becomes very complex and even aperiodic. The values required are not 100's, but of the order of 5. Even for smaller values, peculiarities occur somewhere in the spectrum if  $\delta Q \ge 2$ .

In order to classify and quantify these measurements we have computed the power spectra for selected values of  $\mathbb Q$  and  $\beta$  as a function of  $\beta_T$ . These were computed over 100 periods of the fundamental frequency of the oscillator after a long time interval after turn on, approximately 10° cycles. The Fourier amplitudes, including the dc value of <8>, were calculated. The zero frequency amplitude was then compared to the fundamental frequency which was first calculated in establishing the time interval for the numerical integration. In general the fundamental frequency is <8> $\approx_{\rm Wg}/n$ , except for anharmonic cases for which no fundamental frequency was found. Since the Fourier analysis was performed by a Fourier series no spectra were determined for anharmonic cases.

The power delivered to the load resistance R at any frequency is  $P_i = V_i \cdot / R$  where  $V_i$  is the Fourier amplitude of the voltage at that frequency. The value computed numerically is  $(\theta_i \cdot 2/Q)$  which can be converted to physical units according to the relation

$$P_{\perp} = \omega_0^4 \sigma_0^{2\beta} \frac{1}{2} / (2\pi)^2 I_Q,$$
 (2)

Figure 3 shows the dependence of the power delivered at  $\omega$ , for  $\omega=\omega$  as a function of Q for  $\beta=1$ . The power has the expected maximum near Q=3, decreasing at high Q as Q<sup>-1</sup> and at low Q because of loading of the SQUID. The maximum value of the power is approximately 1.124  $\pm^2 \omega$  /(2 $\pm^2$ ). For design values  $\omega=2\pi \pi 9 \times 10^{-2}$  and L=1pH, this corresponds to 12.9 $\pi$ W. However, as we have seen, values of Q>2 lead to some undesirable spectral response. Nevertheless, even for Q=1, the power is not even diminished by a factor of 2 from the peak value.

Figure 4 shows a compilation of the computed powers of the Josephson oscillations as a function of Q for  $\beta=1$ . In the regime Q<2, the spectra are well

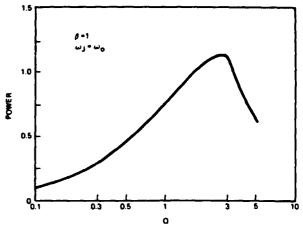


Figure 3. Q-dependence of the power delivered to the load at  $\omega_{j}=\omega_{j}$  for  $\beta=1$  in the resistive SQUID. Power is normalized to  $\left[\omega_{0}^{\phi}\right]^{2}/(2\pi)^{2}$  L.

behaved except for a subharmonic response at  $\omega_1$ =1.2 $\omega_0$ , Q=2. For Q>2, there is a very strong subharmonic response at  $\omega_1$ =2 $\omega_0$ , and anharmonic response near  $\omega_1$ =1.2 $\omega_0$ . For 2<Q<5, the region near  $\omega_1$ =1.2 $\omega_0$  exhibits subharmonic behavior which is not plotted.

The strong subharmonic response when  $\omega_J=2\omega_O$ , which occurs at such modest Q's as 2.5, suggests degenerate parametric oscillations such as those predicted for the SQUID parametric amplifier. This is also suggestive of the period-doubling effects reported to visit nonlinear systems such as Josephson junctions. However, we also observe odd subharmonic responses for Q=5,  $\beta$ =1 which do not fall in the period-doubling class. An example of this is shown in Fig. 5 for Q=5,  $\beta$ =1, and  $\beta$ .=1100.

Fig. 5 for Q=5,  $\beta$ =1, and  $\beta_1$ =1100. These non-period-doubling subharmonics and the nonharmonic response occur in the general vicinity of  $\omega_1$ =1.2 $\omega_2$  for Q=5,  $\beta$ =1; Levinson has recently predicted odd-subharmonic response as the natural form of the instability in highly undamped Josephson junctions. For simulations which were conducted over a wide range of values of  $\omega_1$ , the nonharmonic and odd-harmonic response is very localized in  $\omega_1$ , although we only studied relatively small values of Q. Inspection of e(t) suggests that  $\theta$  spends relatively more time near (2n+1) $\pi$  in this regime, compared to  $2 \ln \pi$  for the more well-behaved regime. Since the junction is basically

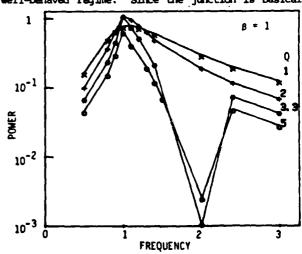
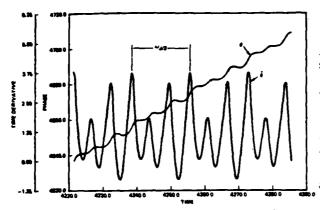


Figure 4. Frequency dependence of the power delivered to the load at  $\omega_{_{\parallel}}$  for selected Q-values with  $\beta\!\approx\!1$  in the resistive SQUID. Power is normalized to  $\left[\omega_{_{\parallel}}\right]_{_{\parallel}}^{0}/\left(2\pi\right)^{2}L\right].$ 

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Figure 5. Computed transient response  $\theta$  and  $\dot{\theta}$  for the resistive SQUID with  $\beta=1$ , Q=5, and  $\beta_{\tau}=1100$ , corresponding to wj=1.1 c. Time is measured in wo

unstable for principal values  $\pi/2 < \theta < \pi/2$ , this may be related to the instability.

The expected effect of increasing  $\beta$  at constant Q=1 is illustrated in the power spectra of Fig. 6. The bracketed symbols are the Josephson frequencies; the symbols connected by straight lines the predicted harmonics. Not all harmonic spectra are shown. Below  $\beta = \pi$ , all computed points are well behaved with some expected harmonics but no subharmonic response. For  $\beta=\pi$ , we observe subharmonics as we did for large Q values at  $\beta = 1$ . For  $\beta > \pi$ , the time dependent response becomes even more complex and we have not computed spectral powers. Such \$-values would be considered outside the normal range for a SQUID and moves toward the non-SQUID Josephson junction for which this analysis indicates very poor quality VCO.

Voltage-Clamped DC SQUID The voltage-clamped dc SQUID of Fig. 1b can be recognized as a combination of two identical resistive SQUIDs with a common biasing resistor r. This leads to strong coupling of the two VCO's with identical frequencies but relative phases determined by the field current I, . Two different modes of the coupled system are essentially a symmetric mode, with the two junctions in parallel, and an antisymmetric mode, with the two junctions in series. Maximum output power and impedance are achieved for the antisymmetric or series mode in which the circulating currents are in the same direction.

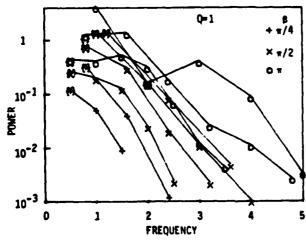


Figure 6. Harmonic spectra for the resistive SQUID with Q=1 and selected \$.

We have carried out simulations of the symmetric voltage-clamped dc SQUID shown in Fig. 1b. The new parameters which define the device performance are  $\beta_0=2\pi LL/\beta_0$ ,  $\beta_1=2\pi LL/\beta_0$ ,  $\theta_1=(\theta_0+\theta_1)/2$  and  $\theta_0=(\theta_0-\theta_1)/2$ .  $\theta_0$  and  $\theta_1$  refer to the phases of the right and left hand junctions, respectively, and we have defined  $\beta$  as  $2\pi LL/\beta_0$ , where L is the inductance of one-half of the dc SQUID. In a limited number of simulations with S=1 and S;=0, the dc SQUID behaves identically to the resistive rf SQUID with  $\theta_{-}=\dot{\theta}_{-}=0$ . The two junctions switch in-phase with one another and the resulting currents in each L add in the small resistance r. However, the voltage across the output (2L) is identically zero.

For  $\beta_1=\pi/2$  and  $\beta=1$ , the computation shows that responds at the expected Josephson frequency while responds at the second harmonic. This frequency doubling is a result of the alternate switching of the two junctions in each Josephson period. We have computed the response at  $\beta_1=\pi/2$ ,  $\beta=1$  for a range of Q from 1 to 5; the behavior of  $\theta$  and  $\theta$  is very similar to that observed for  $\theta$ ,  $\theta$  in the resistive SQUID and

will not be reproduced here.

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41.7

SQUID Array VCO
Arrays of SQUIDs, driven coherently, can achieve further increase in power. Three types of arrays are possible: the longitudinal dc SQUID array, the transverse dc SQUID array, and the resistive SQUID bus. We discuss these briefly Preliminary descriptions of the longitudinal and transverse do SQUID arrays were given previously in connection with parametric emplification. Figure 7 shows the simple circuits for both the linear dc SQUID arrays, and for a two-dimensional array which combines the two linear arrays. Analysis of the longitudinal flux-flow array has shown that it is unstable with respect to the flux-flow mode which is the one required for the VCO. have shown, this type of array can As Sandell, et al provide series increase in rf impedance although it requires large do currents because of its parallel nature at dc. Furthermore, in a tunnel junction implementation as compared with microbridges, narrow linewidths will require small biasing resistors as in the voltage-clamped dc SQUID. The transverse array is recognized as a series array of dc SQUID pairs. It provides increased impedance at both rf and dc, offering a reduction in the direct current required. Again, narrow linewidths will require small biasing resistors. It would be undesirable to fabricate large arrays requiring large numbers of carefully matched small resistors.

A method of avoiding the resistor network and producing a high power array is the resistive SQUID bus as shown in Fig. 7. It is an extension of the voltage-clamped dc SQUID to N resistive SQUIDs with a common voltage-biasing resistor. This could be readily accomplished in a thin film format with a pillbox resistor at the center of a radial array of SQUIDs. These might drive a coaxial line to an off-chip load

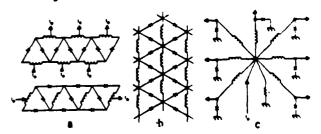


Figure 7. Schematic diagrams of SQUID arrays. Longitudinal (upper) and transverse (lower) fluxflow arrays; (b) Two dimensional array; (c) SQUID bus.

On-chip, the bus can be used to drive many different loads which require coherent input, as in clocking a shift register or driving an array of mixers. If one supplies internal phase shifts of  $\pi$  between adjacent elements (as in the dc SQUID), then one can supply alternate out-of-phase signals. In either case, coherence is guaranteed by the common bias resistor and superconducting phase coherence.

Summary

The SQUID is the natural form for signal generation via the Josephson effect. While the bare Josephson junction suffers from both chaotic instabilities and low impedance, its incorporation in a low β, low Q SQUID tunes out the junction capacitance and controls the instability by directly harnessing the periodic nature of the junction phase to the quantum periodicity of the SQUID magnetic flux. This results in an efficient conversion of the Josephson energy to the circulating current in the SQUID inductance and, hence, to external circuitry. The power is maximized by minimizing the inductance, although at further reduction in source impedance. This last problem is alleviated by forming a priori coherent, stable arrays of SQUIDs to increase both the total power and impedance. Such arrays have the voltage-clamped dc SQUID as the basic cell. The design presented makes minimum demands on both lithography and Josephson current density.

Several experimental results have been reported in recent years which pertain to this device. The group at SUNY have studied microbridge VCO's, including pairs and arrays. The most successful results were in the dc SQUID-like arrays' reminiscent of Fig. 7a, although the SQUID inductances were very large compared to the values suggested here. Calander and Zappe proposed using a dc SQUID as a tunable oscillator for intracomputer communication in a Josephson processor, They predicted ~lwW of power at 500 GHz. Tuckerman demonstrated this concept by connecting a dc SQUID transmitter with another dc SQUID receiver by a superconducting microstripline. The transmitted microsave signals were readily detected. Calendar, et al. demonstrated a resistive source. demonstrated a resistive SQUID VCO incidentally to their self-pumped Josephson parametric amplifier. Using a SQUID with 8 =2, L=3pH, r=3x10^-2 $\Omega$ , and a 1 $\Omega$ matching transformer, they observed 0.15mW at 10.4 GHz with a bendwidth of 150 MHz. This linewidth is much greater than the thermal-noise-limited value, 5 MHz, and is reportedly broadened by noise in the bias current. Assuming that the average power is diminished by the same factor as the line broadening, one could have expected as much as 4.5mW, in excellent agreement with the predictions of the model.

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S. F. Carrier Water Contract

## APPENDIX B

Printout of the computed characteristics of the resistive SQUID VCO. The first line shows the run number,  $\beta$ , Q,  $\beta_I$ , computed fundamental frequency, and subharmonic number relative to the Josephson frequency; the second line is the computed spectral power for the first seven harmonics of the fundamental frequency. Where the frequency and harmonic number are reported as zero, the oscillator is anharmonic.

Run# _	ß	Q	β <sub>1</sub>	<b>ਘ/ਘ</b> ₀	Subh	armonic#	
P		P <sub>2</sub>	P <sub>3</sub>	P <sub>4</sub>	P <sub>5</sub>	P <sub>6</sub>	P <sub>7</sub>
1 1.215E-	0.78 01 5.	<b>Q</b> <b>P<sub>2</sub></b> 1.00 192E-02 9.8	500 525-83	4.993E-6 6.037E-04	01   4.405E-05	1 4.121E-06	3.816E-07
2	0.78	1.00	800	7.989 <b>E-</b> 0	01	1	5.115E-09
3.369E-	01 4.	023E-0. 1.7	66E-83	5.3126-05	2.401E-06	1.111E-07	
3	0.78	1.00	1000	9.987E-0	01	1	4.479E-10
5.172E-	01 2.	063E+02 5.1	23 <b>E-</b> 04	1.546E-05	4.701E-07	1.446E-08	
4 4.861E-	0.78 01 9.	1.00 296E-03 1.5	1200 97E-04	1.199E ( 2.920E-06	00 5.434E-03 \	1 1.019E-09	1.917E-11
5	0.78	1.00	2006	1.998E (	00	1	5.812E-17
1.885E-	01 4.	6805-04 1.2	315-86	3.2795 09	8.769E-12	2.344E-14	
6	1.57	1.00	500	4.989E-0	01	1	1.489E-04
2.686E-	01 2.	043E-01 1.3	:04E-01	2.566E-02	2.4335-60	€.343E-04	
7	1.57	1.00	800	7.985E-0	)1	1	4.391E-06
7.978E-	01 3.	118E-01 2.1	04E-02	2.274E-03	3.098 <b>E-U4</b>	3.683 <b>E-05</b>	
8	1.57	1.00	1000	9.982E-0	)1	1	7.360E-07
1.476E	00 1.	644E-01 1.1	50E-62	1.064E-03	9.289E-05	8.231E-06	
9	1.57	1.00	1200	1.198E 6	0	1	5.900E-08
1.500E	00 8.	592E-02 4.9	32E-03	2.864E-04	1.6925-05	9.952 <b>E-0</b> 7	
10	1.57	1.00	2000	1.998E 0	)0	1	6.794E-13
6.933E-	01 6.	667E-03 6.6	598-05	6.709E-07	6.783E-09	6.850E-11	
11	3.14	1.00	500	4.077E-0	01	1	4.442E-03
4.494E-	01 4.	154E-01 5.3	1936-01	3.286E-01	6.939E-02	1.259E-02	
12 1.273E	00 1.	1.00 396E 00 1.9	806   10E-01	7.974E-6 2.640E-02	01 1.132E-02	1 2.722E-03	6.296E-04
13	3.14	1.00			91	1	
		Copy avai	y legible :	TIC does not reproduction			

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3.309E 00 5.415E-01 1.225E-01 2.301E-02 4.516E-03 8.823E-04 1.723E-04
                            0.000E 00
      3.14
              1.00
                    1200
0.000E 00 0.000E 00 0.000E 00 0.000E 00 0.000E 00 0.000E 00 0.000E 00
                            9.984E-01
              1.00
                    2000
      3.14
4.324E 00 1.621E-01 4.091E-01 8.964E-02 3.360E-03 1.999E-03 9.504E-04
                            9.900E-01
      1.00
              0.10
                    1000
9.948E-02 2.472E-04 5.929E-07 1.516E-09 1.097E-11 4.057E-12 3.435E-12
                     500
                             4.978E-01
     1.00
             0.25
1.950E-01 1.115E-02 5.591E+04 2.694E-05 1.422E-06 1.172E-07 2.789E-08
     1.00 0.25
                             9.9578-61
18
                    1000
2.428E-01 3.274E-03 3 638E-05 3.835E-07 7.727E-09 1.178E-09 7.541E-10
19 1.00 0.25 2000 1.992E 00
2.185E-01 4.641E-04 9.824E-07 2.314E-08 1.016E-08 7.187E-09 5.416E-09
20 1.00 0.25 3000 2.988E 00
1.734E-01 9.857E-05 6.609E-08 2.313E-09 1.207E-09 9.153E-10 6.811E-10
                           9.963E-01
   1.00 0.30 1000
2.881E-01 5.249E-03 7.095E-05 9.814E-07 1.934E-08 1.904E-09 9.791E-10
                            9.974E-01
22 1.00 0.50 1000
4.555E-01 1.690E-02 4.041E-04 1.040E-05 4.838E-07 8.373E-08 4.168E-08
23 1.00 1.00 500
                            4.992E-01
1.660E-01 9.371E-02 2.601E-02 2.561E-03 2.436E-04 3.104E-05 4.096E-06
     1.00
             1.00 800
                            7.998E-01
24
5.030E+01 8.673E+02 8.576E+03 2.270E+04 1.516E-05 1.013E+06 6.715E-08
25 1.00 1.00 900
                            8.987E-01
6.640E-01 6.425E-02 2.416E-03 1.367E-04 7.497E-06 4.143E-07 2.311E-08
26 1.00 1.00 1000 9.986E-01
7.694E-01 4.550E-02 1.675E-03 7.656E-05 3.474E-06 1.593E-07 7.367E-09
27 1.00 1.00 1100 1.099E 00
7.857E-01 3.148E-02 1.028E-03 3.704E-05 1.350E-06 4.969E-08 1.837E-09
28 1.00 1.00 1200 1.198E 00
7.423E-01 2.146E-02 5.723E-04 1.607E-05 4.583E-07 1.316E-08 3.791E-10
29 1.00 1.00 1403
                            1.398E 00
6.024E-01 9.828E-03 1.615E-04 2.723E-06 4.646E-03 7.962E-10 1.367E-11
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